

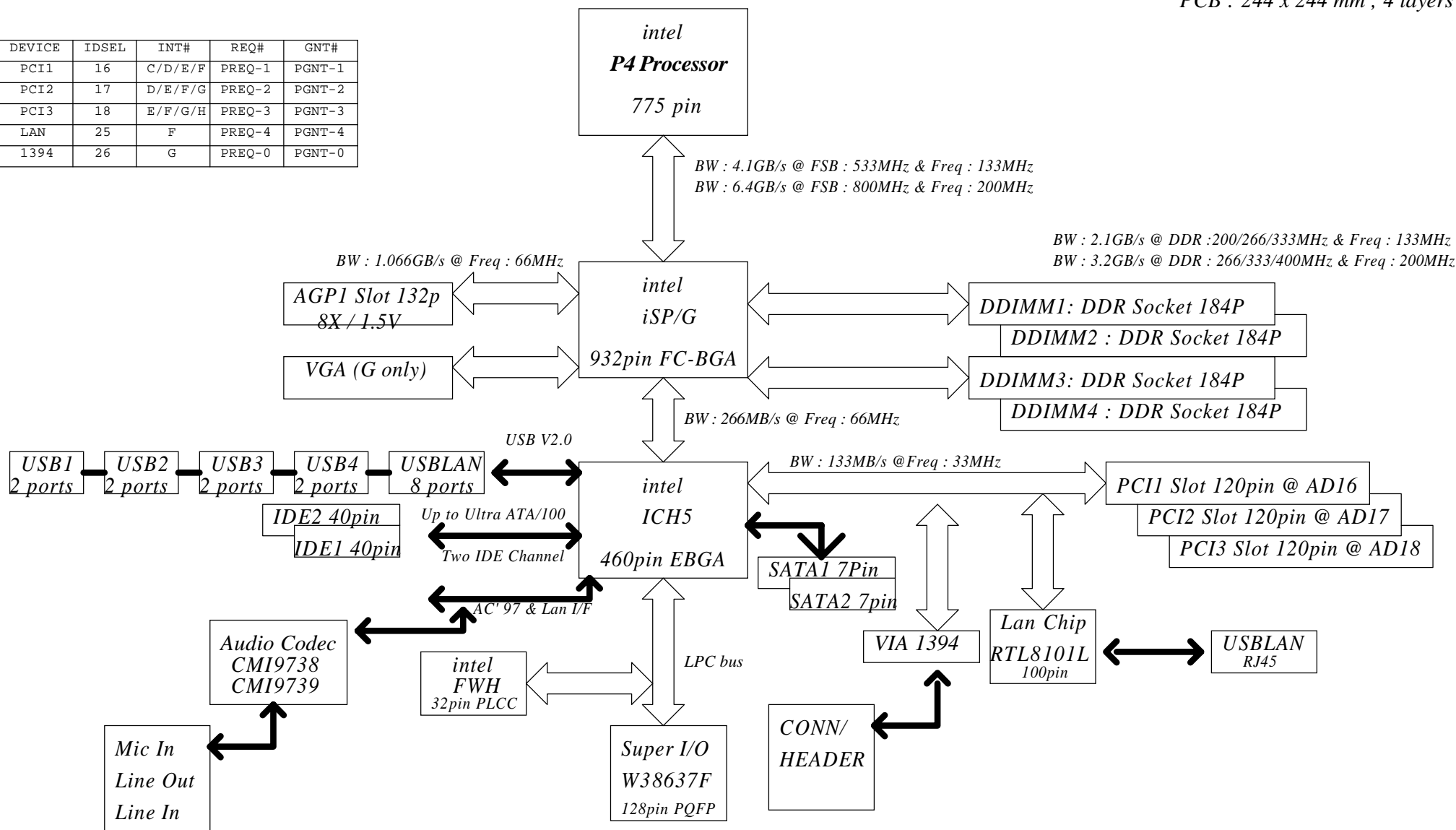
**Schematics Version History Table :**

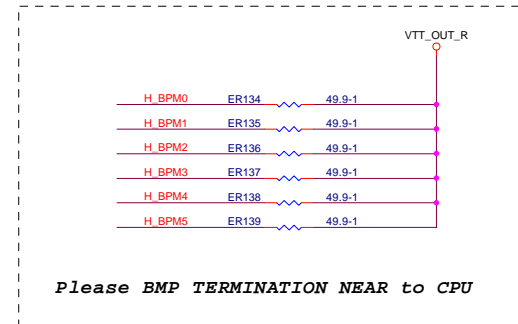
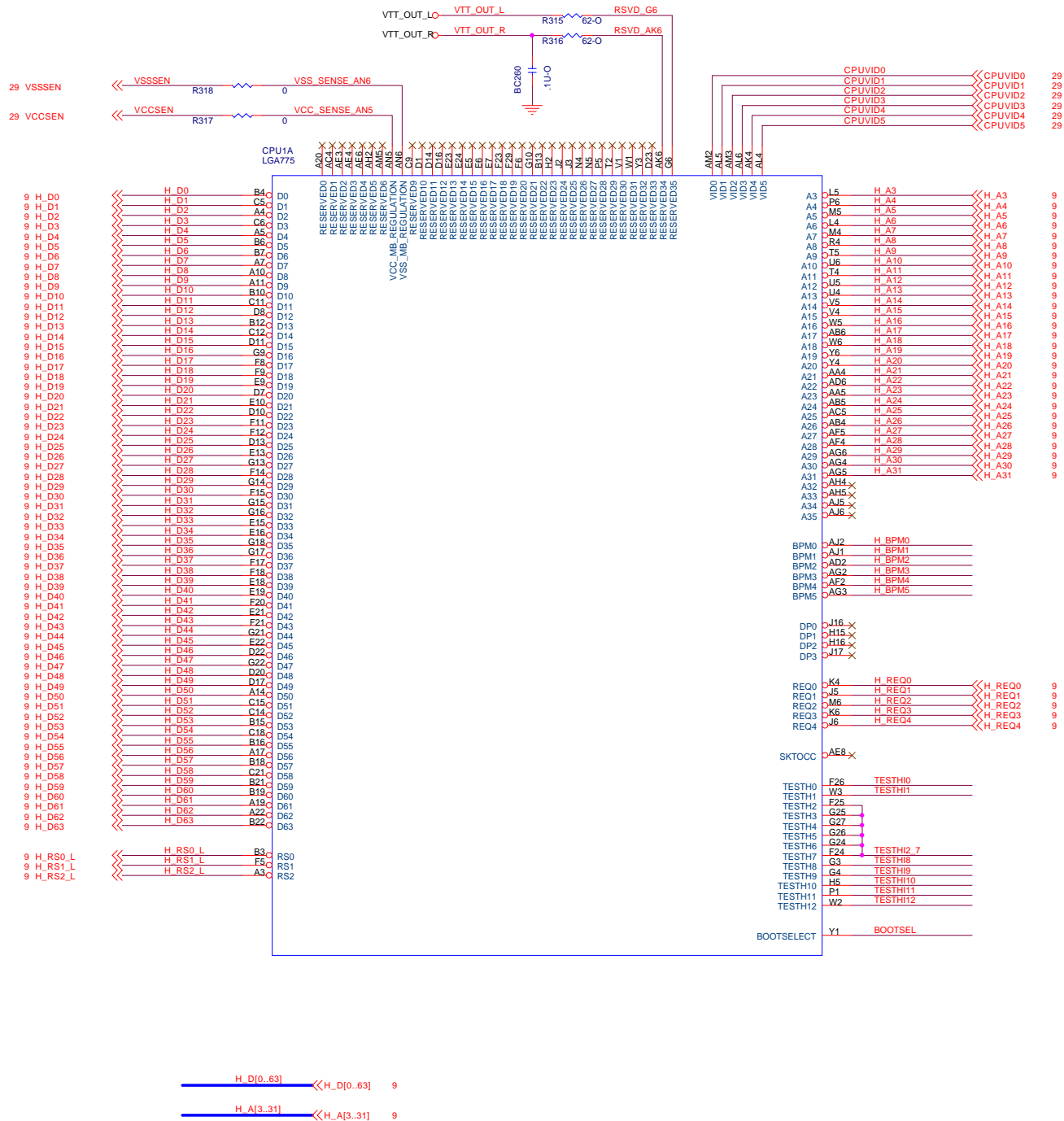
<i>Circuit Ver.</i>	<i>PCB Ver.</i>	<i>Total Page</i>	<i>Modificatory Page</i>	<i>Date</i>
<i>A</i>	<i>A</i>	<i>33</i>		<i>04/16 / ' 04</i>
<i>B</i>	<i>B</i>	<i>33</i>		<i>05/19 / ' 04</i>
<i>1.0</i>	<i>1.0</i>	<i>33</i>		

**Page Title of Schematic :**

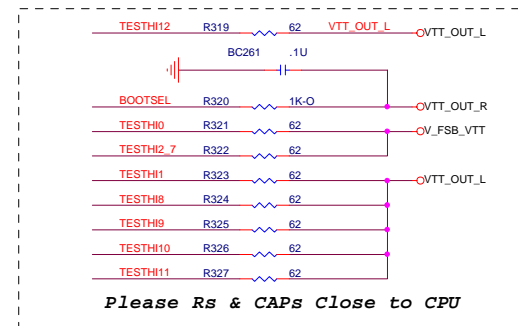
<i>Title</i>	<i>Page</i>	<i>Title</i>	<i>Page</i>
<i>Cover Sheet</i>	<i>1</i>		
<i>System Block Diagram</i>	<i>2</i>		
<i>P4 775P Part A</i>	<i>3</i>		
<i>P4 775P Part B</i>	<i>4</i>		
<i>P4 775P Part C</i>	<i>5</i>		
<i>P4 775P Part C</i>	<i>6</i>		
<i>P4 775P Part C</i>	<i>7</i>		
<i>Clock Generator</i>	<i>8</i>		
<i>I865(MCH)Part A</i>	<i>9</i>		
<i>I865(MCH)Part B</i>	<i>10</i>		
<i>DDR Serial Resistors</i>	<i>11</i>		
<i>DDIMM 1&amp;2 ( DDR SDRAMs )</i>	<i>12</i>		
<i>DDR SSTL-2 Terminations</i>	<i>13</i>		
<i>DDR Power</i>	<i>14</i>		
<i>AGP 8X Slot 1.5V</i>	<i>15</i>		
<i>ICH5 Part A</i>	<i>16</i>		
<i>ICH5 Part B</i>	<i>17</i>		
<i>ICH5 Part C</i>	<i>18</i>		
<i>IDE Connector</i>	<i>19</i>		
<i>USB/FWH</i>	<i>20</i>		
<i>LPC_FDD/KB/M</i>	<i>21</i>		
<i>I/O Ports</i>	<i>22</i>		
<i>H/W Monitor</i>	<i>23</i>		
<i>AC97 Codec</i>	<i>24</i>		
<i>Audio Interface</i>	<i>25</i>		
<i>ATX Power &amp; Front Panel</i>	<i>26</i>		
<i>LAN/CNR</i>	<i>27</i>		
<i>775P Vcore DC-DC</i>	<i>28</i>		
<i>MIS DC-DC</i>	<i>29</i>		
<i>PCI Slot 1&amp;2</i>	<i>30</i>		
<i>PCI Slot 3</i>	<i>31</i>		
<i>VT6307 (1394)</i>	<i>32</i>		
<i>1394 Connector</i>	<i>33</i>		

DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	16	C/D/E/F	PREQ-1	PGNT-1
PCI2	17	D/E/F/G	PREQ-2	PGNT-2
PCI3	18	E/F/G/H	PREQ-3	PGNT-3
LAN	25	F	PREQ-4	PGNT-4
1394	26	G	PREQ-0	PGNT-0



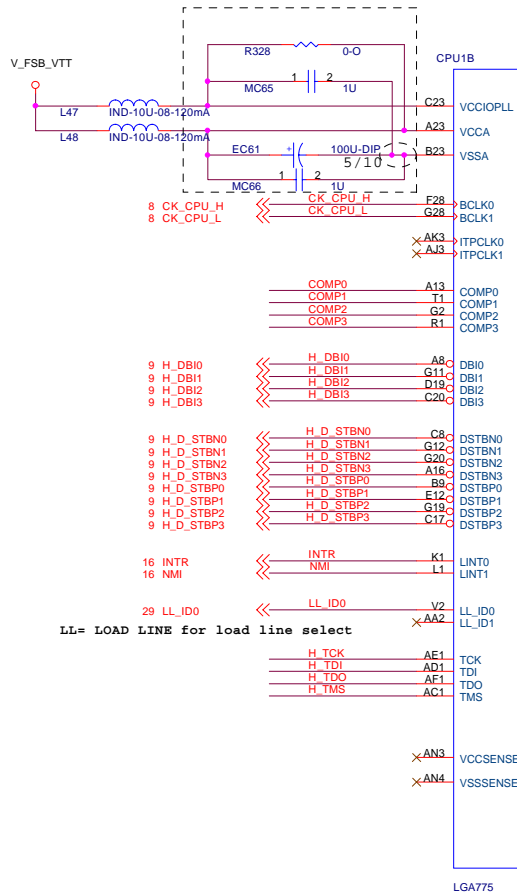


Please BMP TERMINATION NEAR to CPU

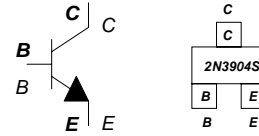
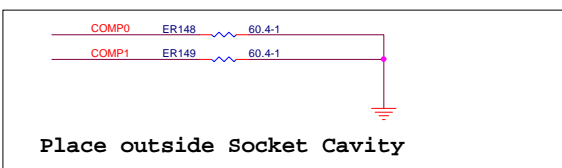
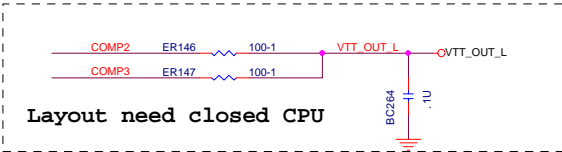
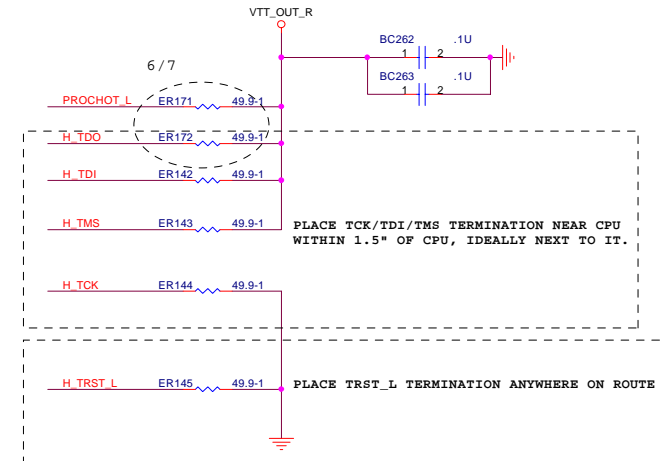
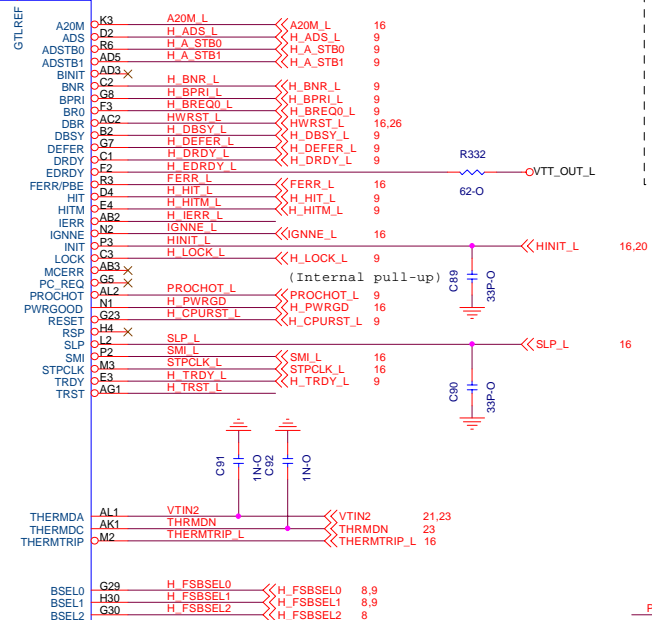
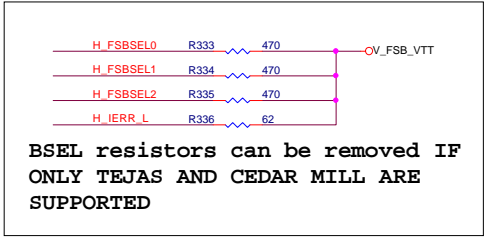
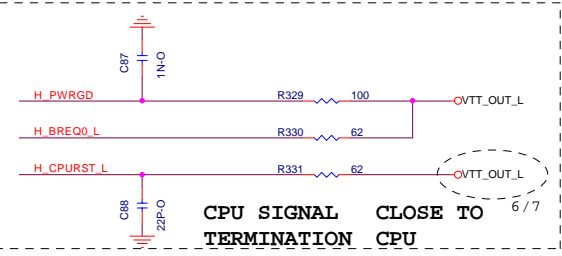
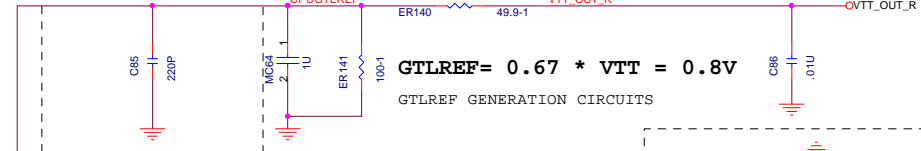


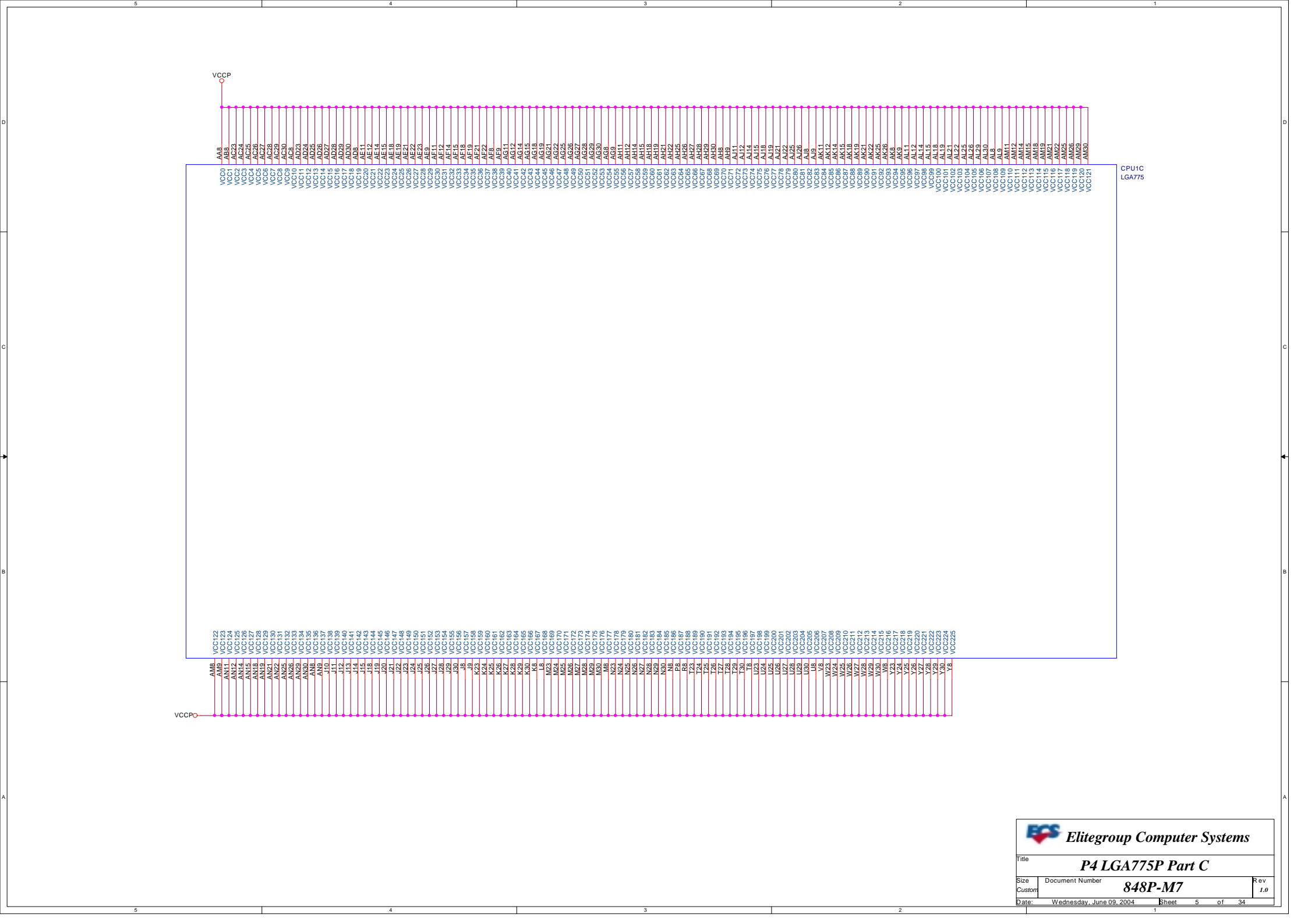
Please Rs & CAPs Close to CPU

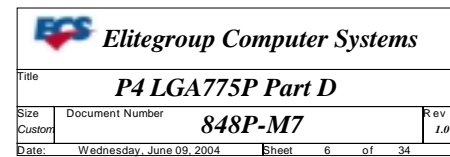
Place componets as close as possible to Processor socket  
trace width to cap must be no smaller than 12 Mils

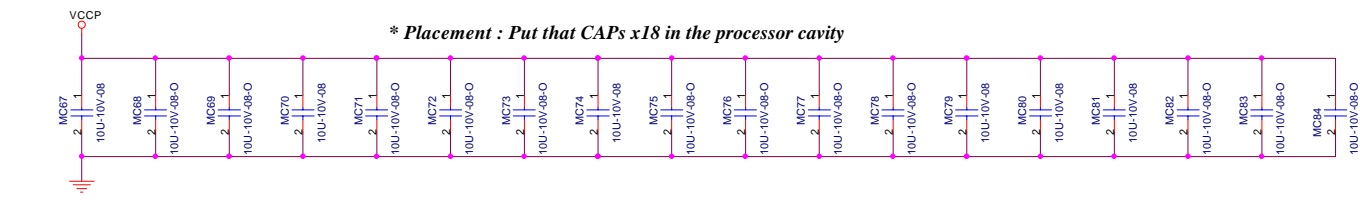


Close CPU H1 pin

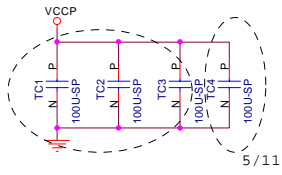




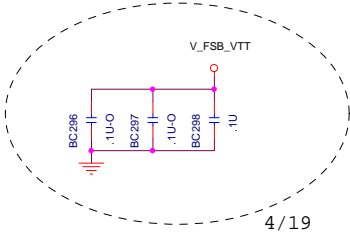
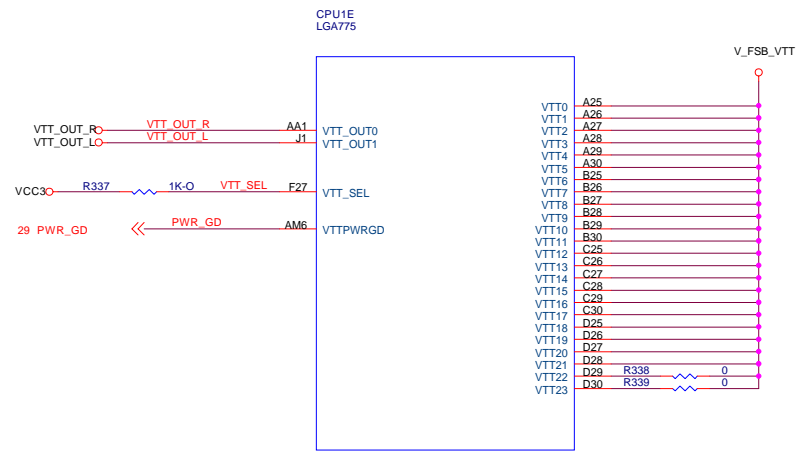


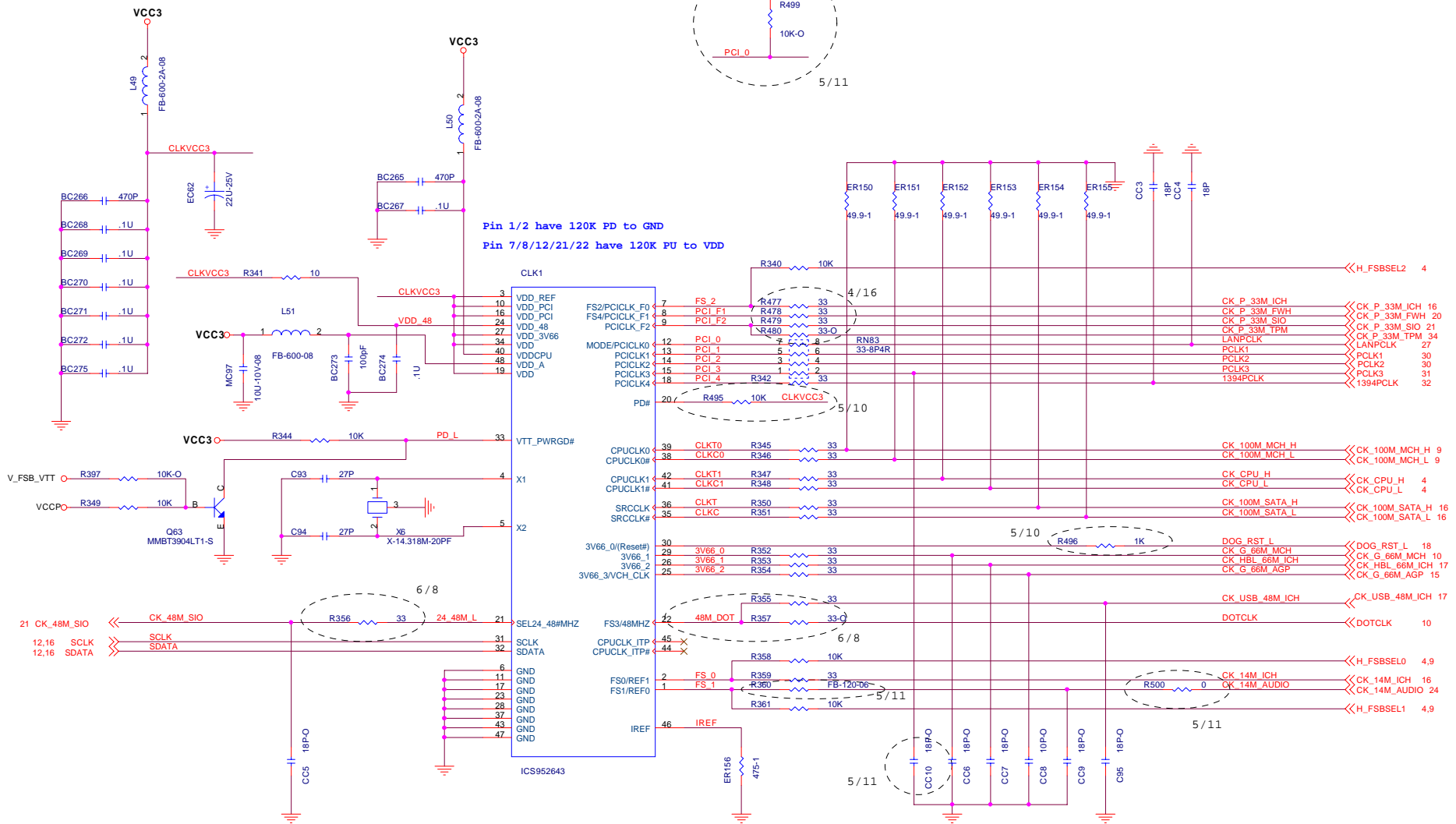


\* Placement : Put that CAPs x18 in the processor cavity



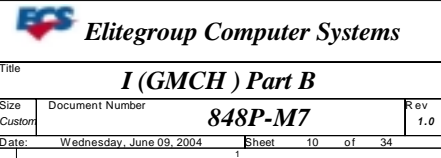
VTT\_SEL=0 for the Tejas processor

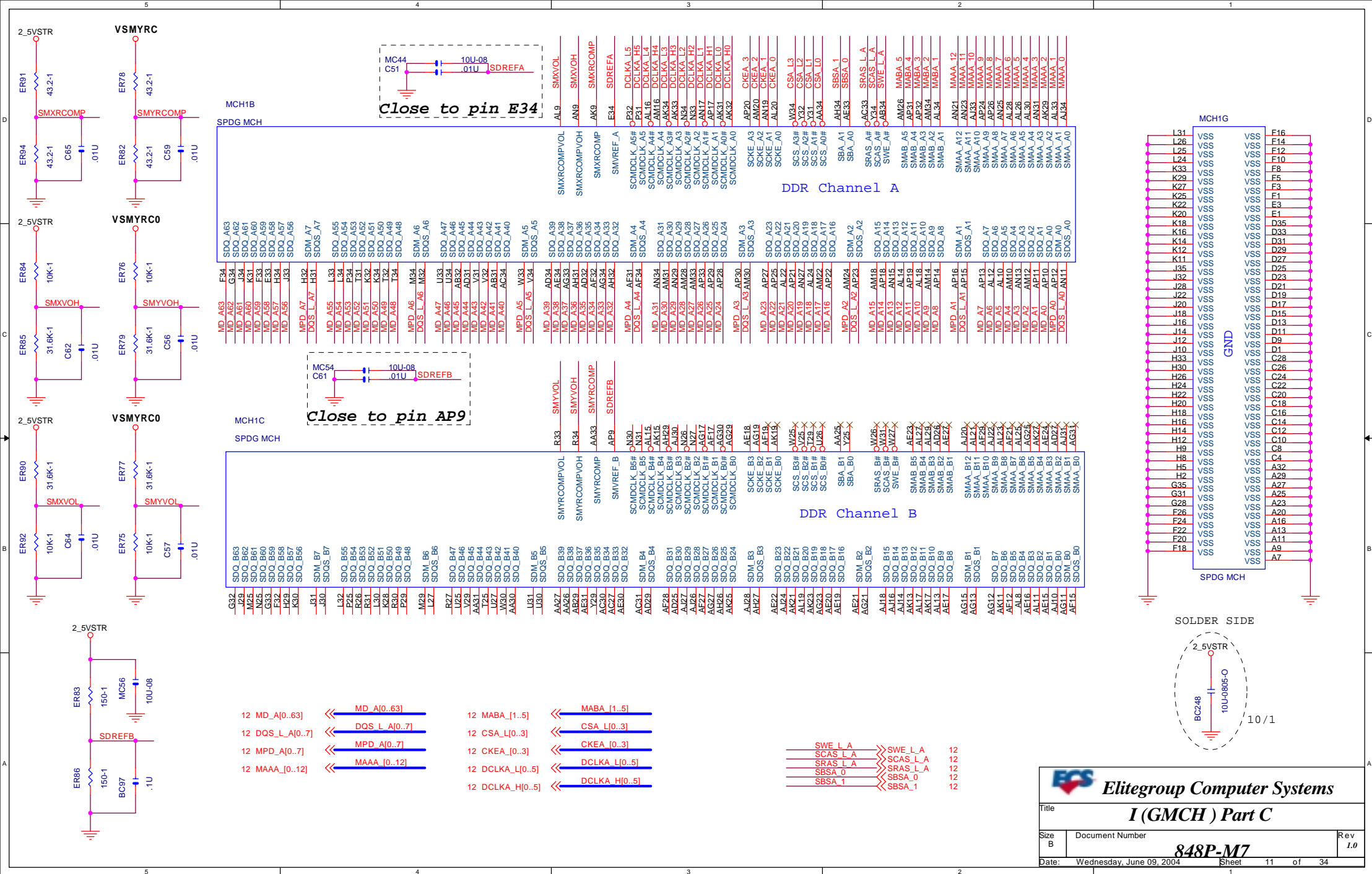









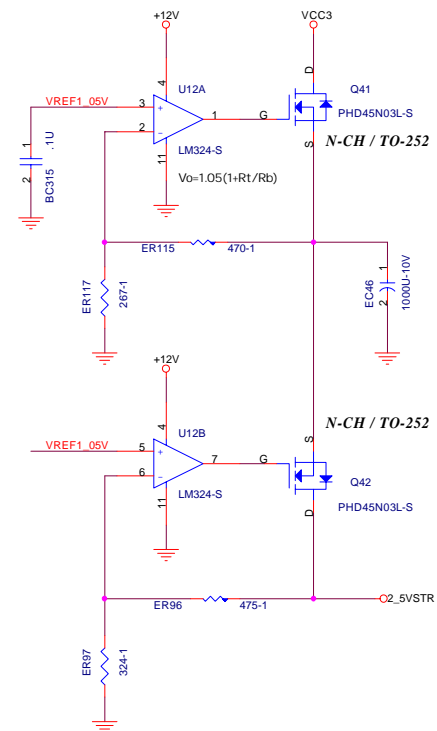
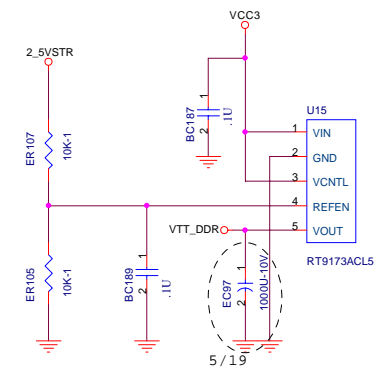






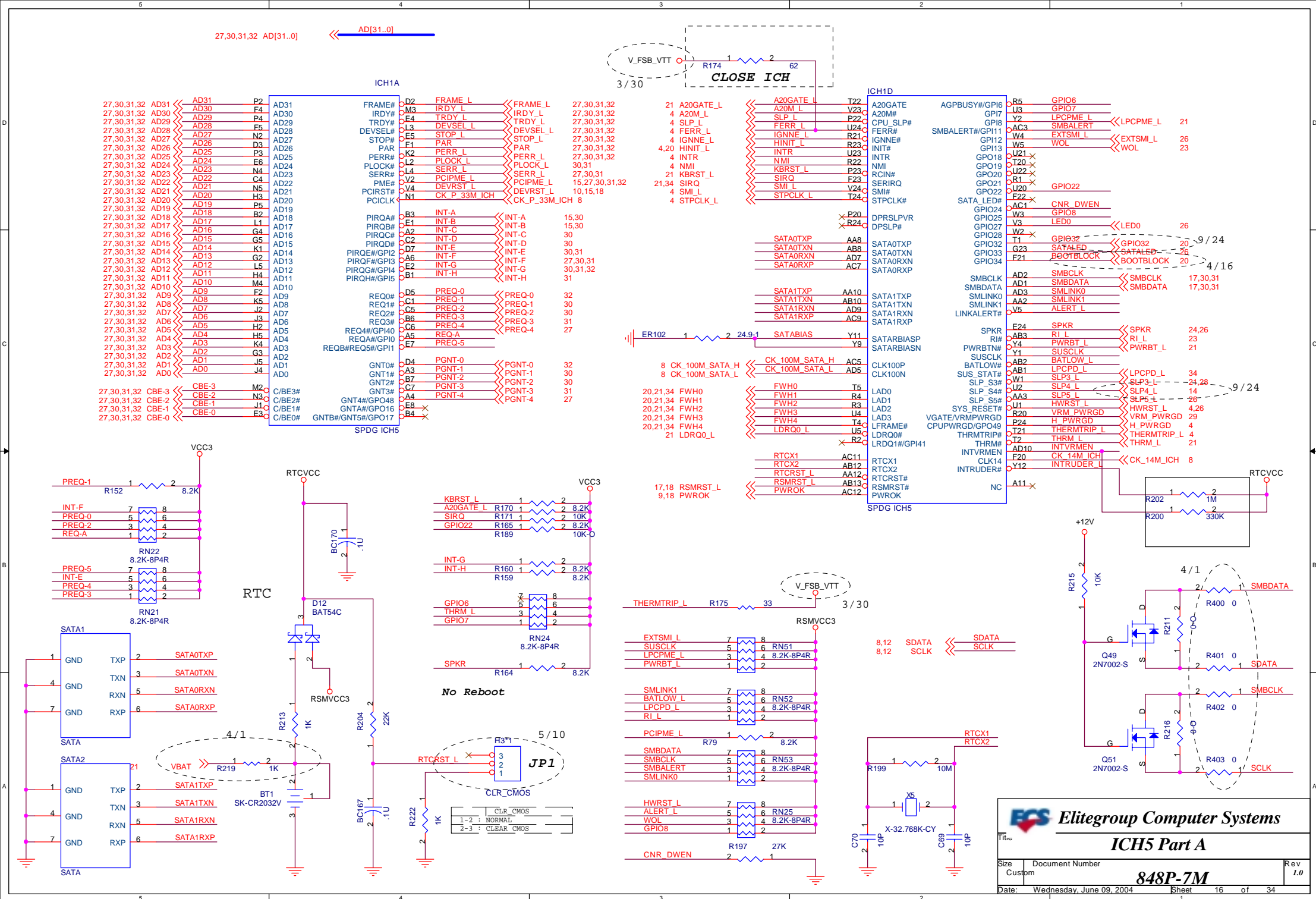
	A	B	C	D	E
4					
3					
2					
1					

 <b>Elitegroup Computer Systems</b>		
Title <b>DDIMM 3&amp;4 ( DDR SDRAMs )</b>		
Size Custom	Document Number <b>848P-M7</b>	Rev <b>1.0</b>
Date:	Wednesday, June 09, 2004	Sheet 13 of 34



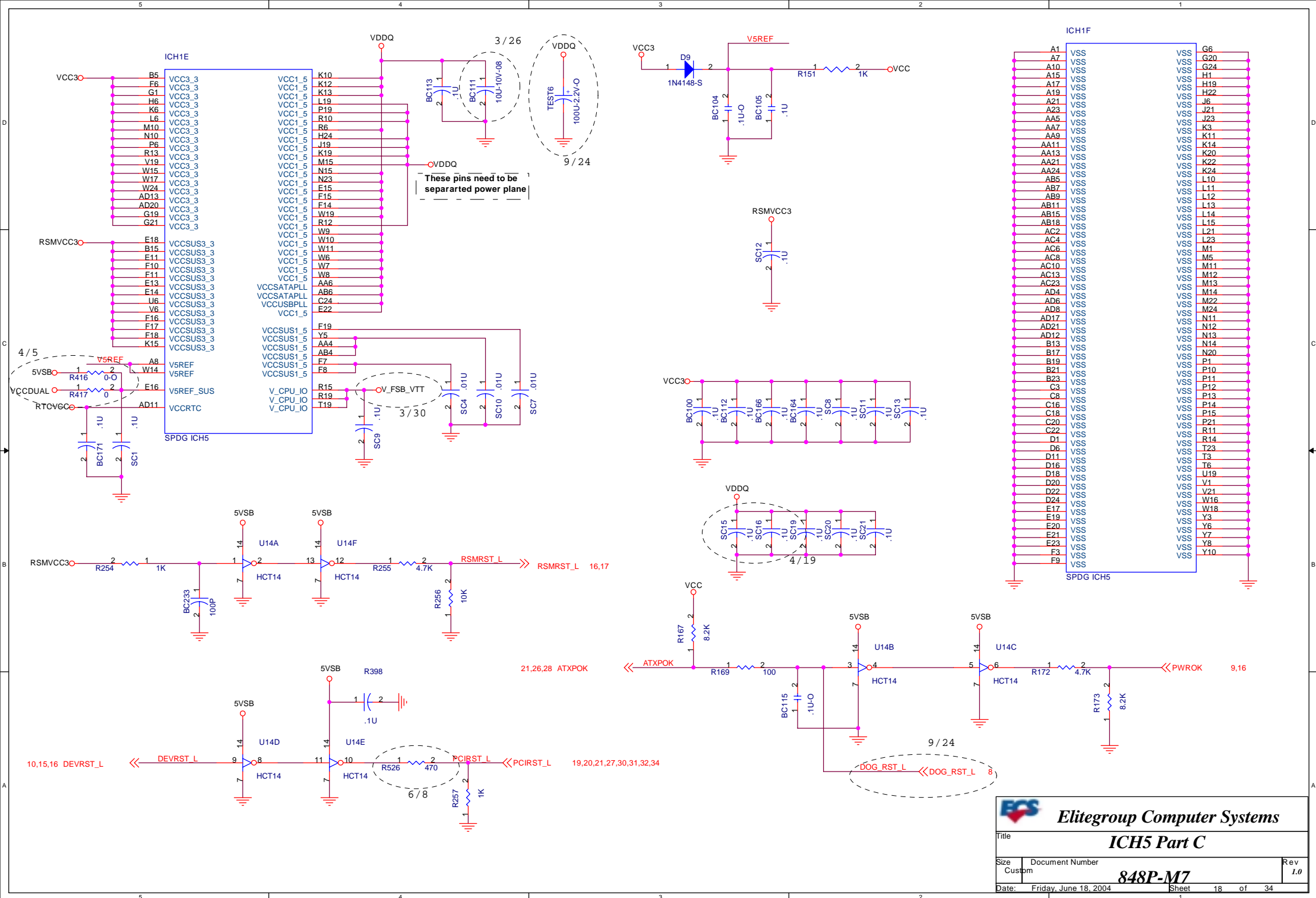


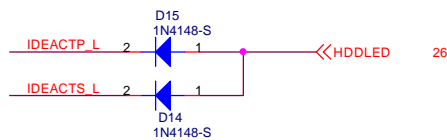
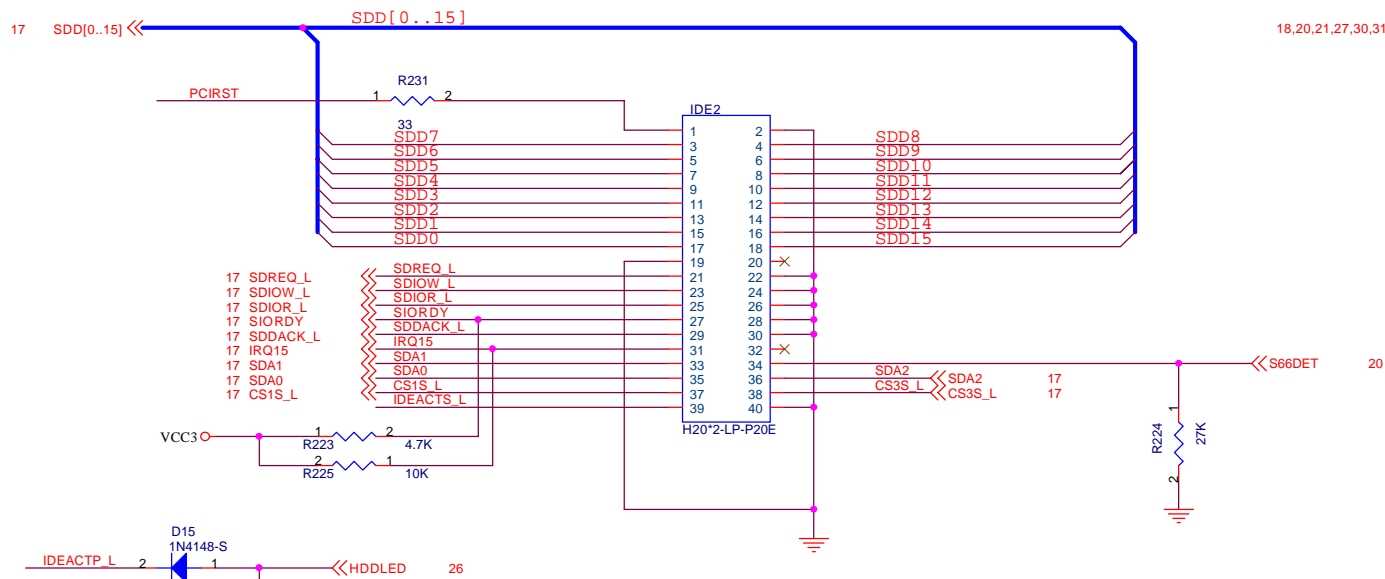
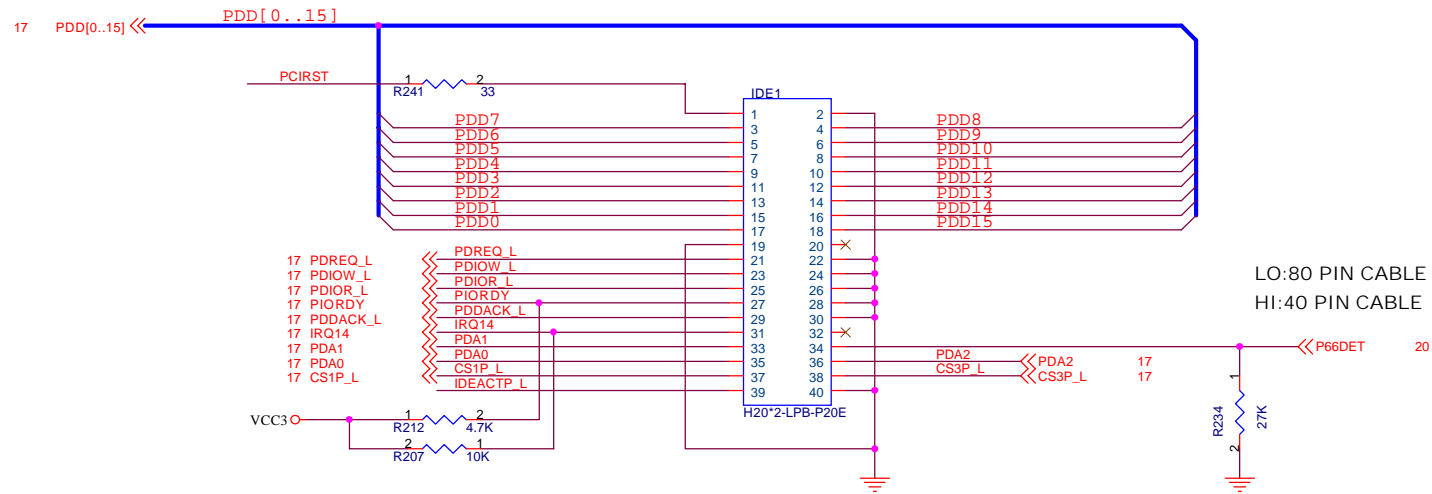












# MAX TRACE LENGTH IS 8"

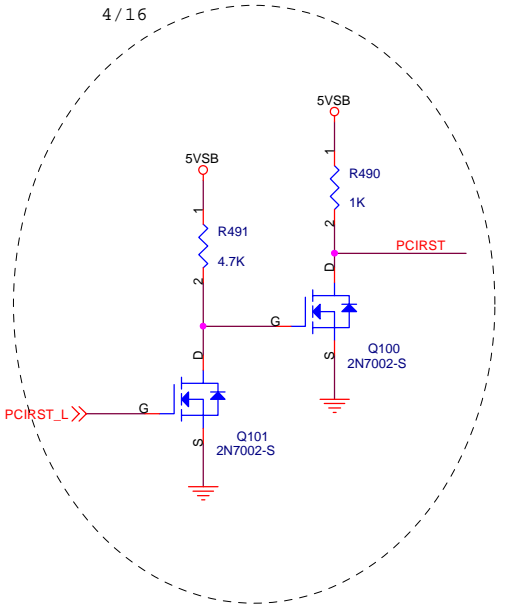
4/16

LO:80 PIN CABLE  
HI:40 PIN CABLE

P66DET 20

18,20,21,27,30,31,32,34 PCIRST\_L >>

S66DET 20



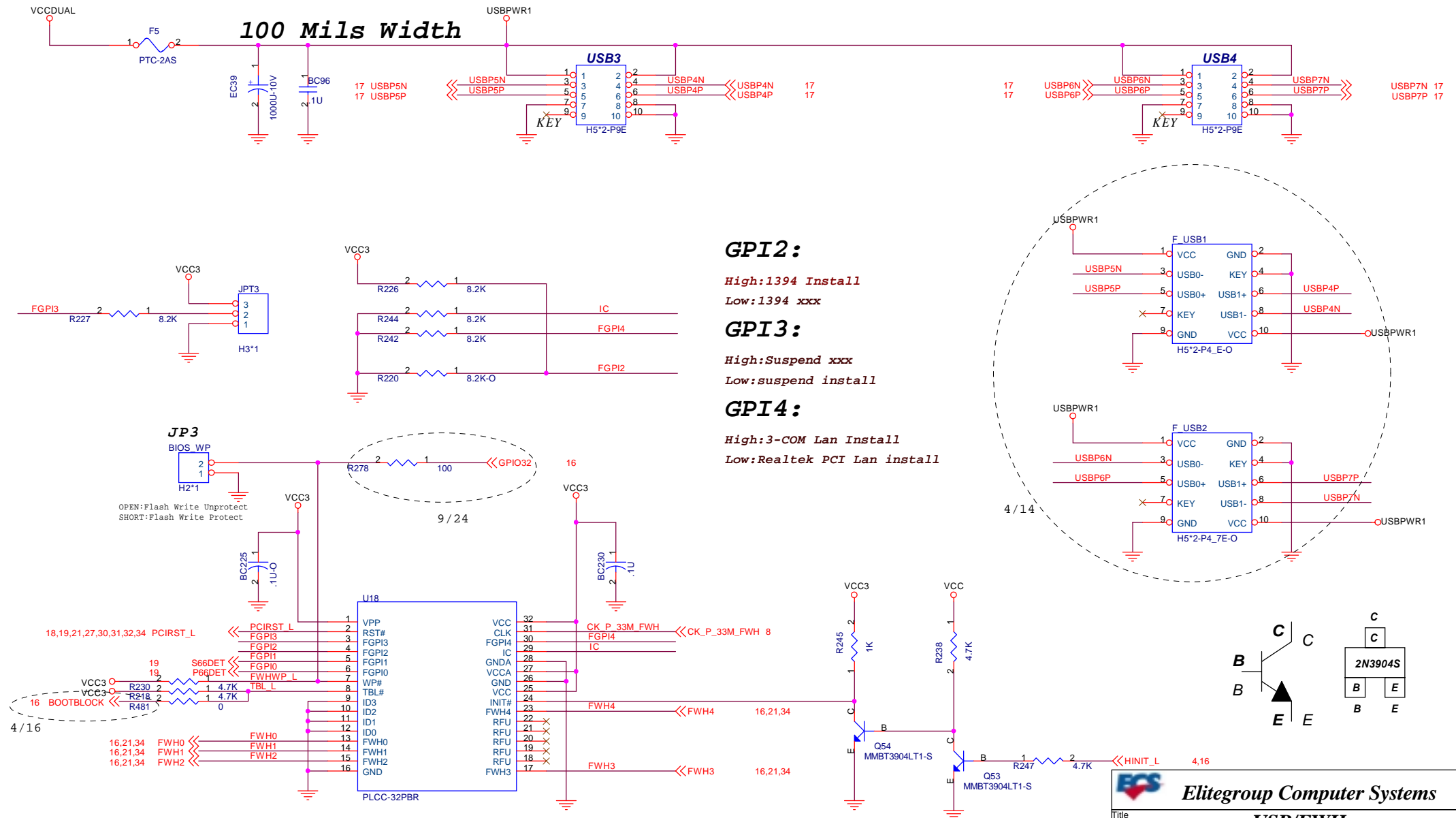
Elitegroup Computer Systems

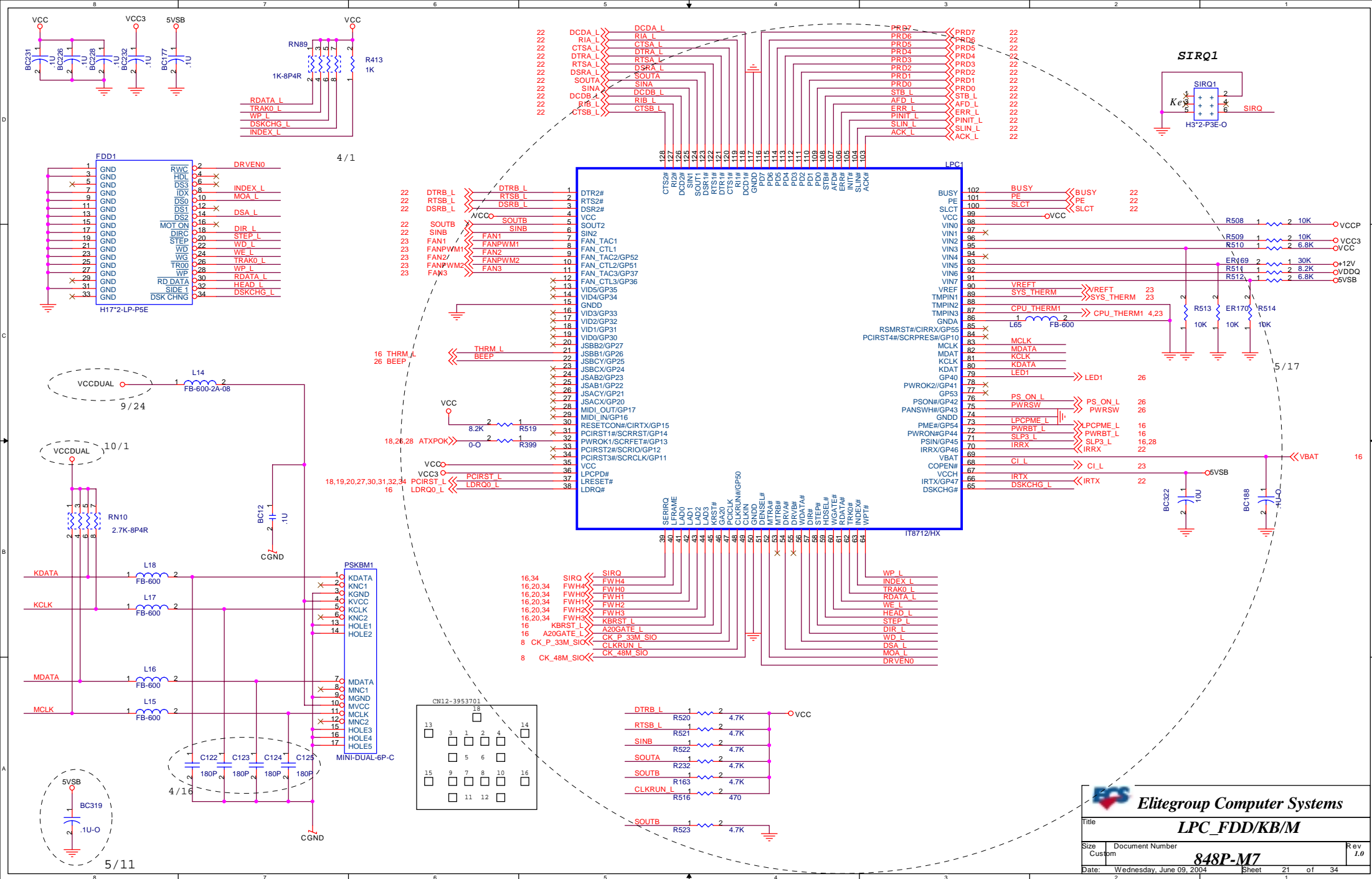
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Size Custom Document Number 848P-M7 Rev 1.0

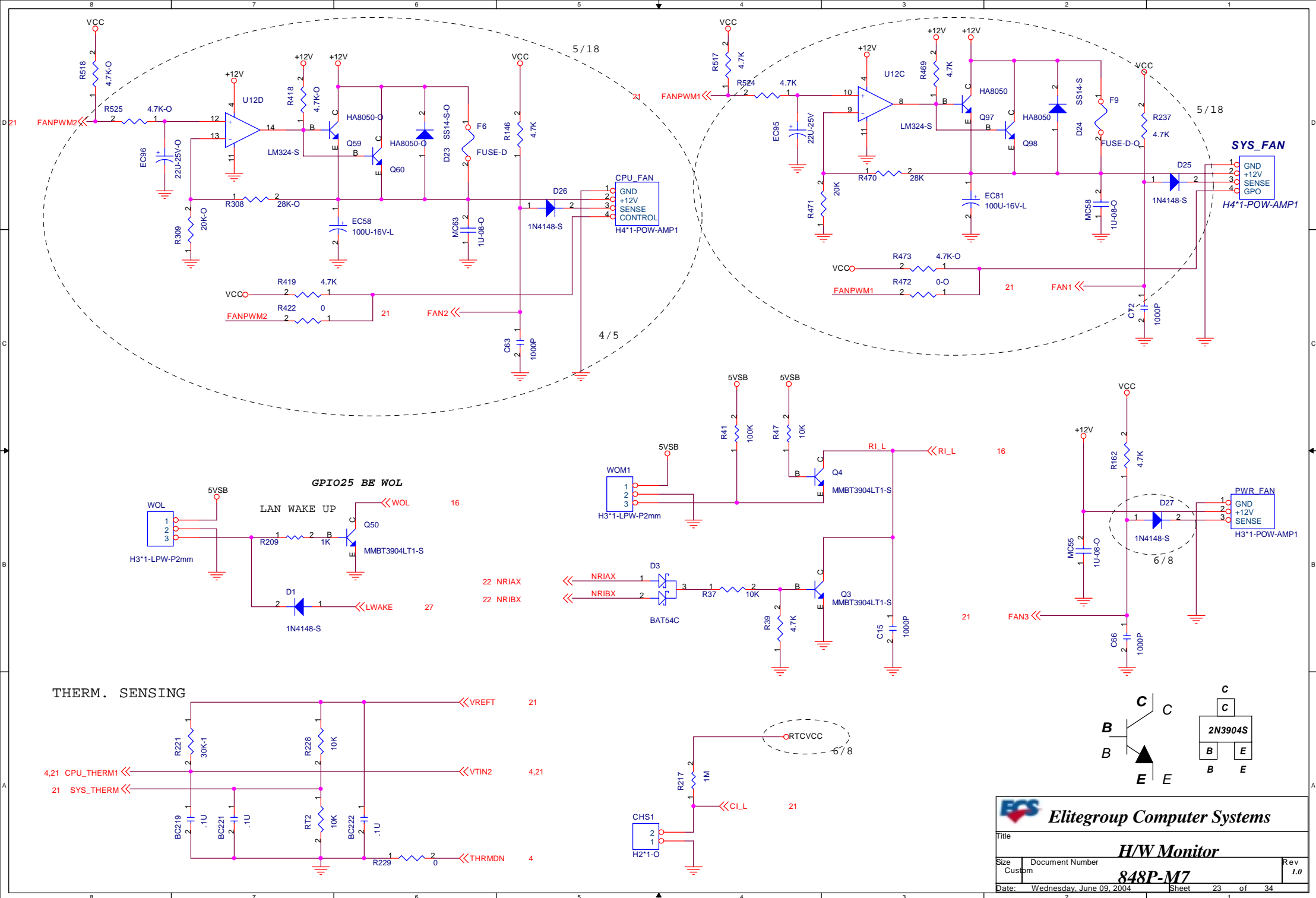
Date: Wednesday, June 09, 2004 Sheet 19 of 34

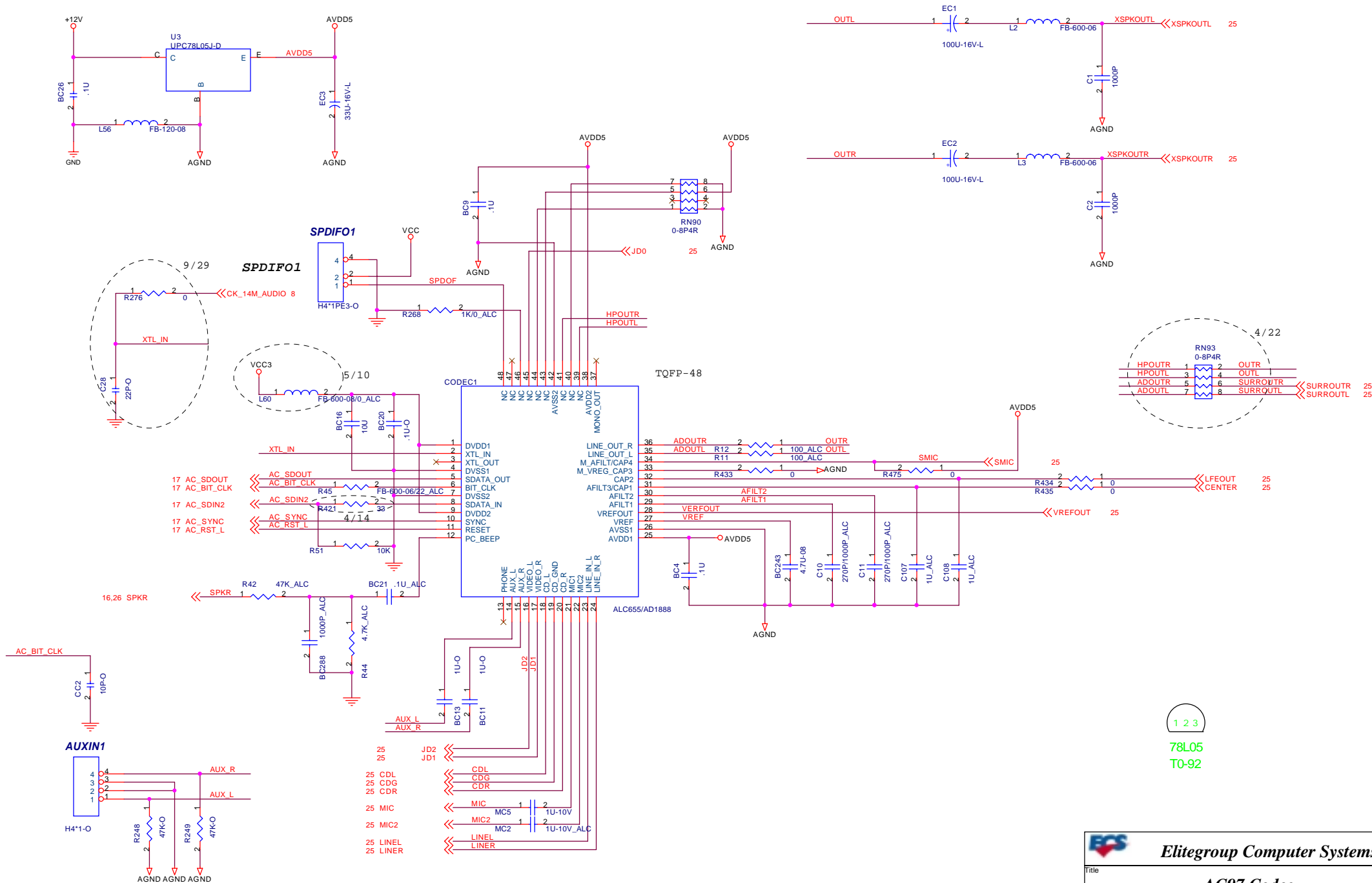
## USB PORT INTERFACE









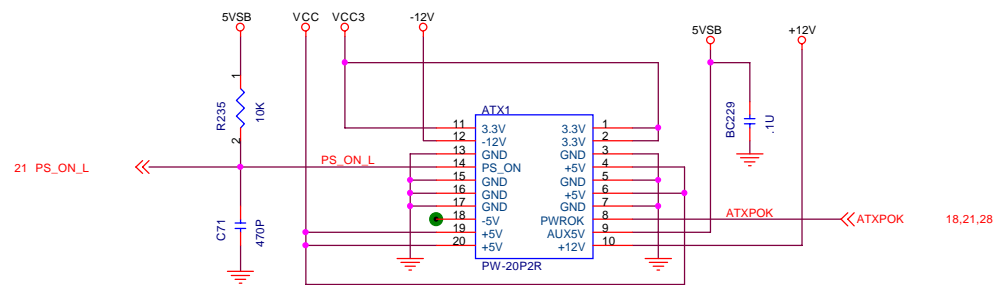


1 2 3

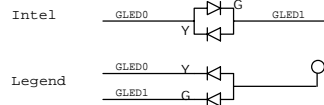
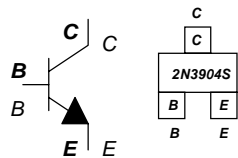
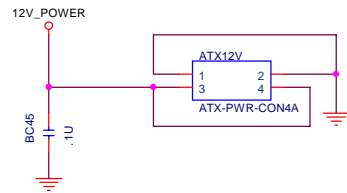
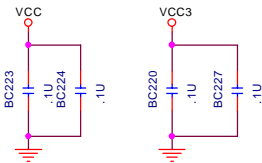
78L05  
T0-92



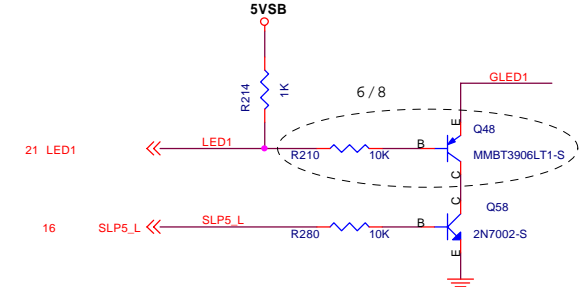
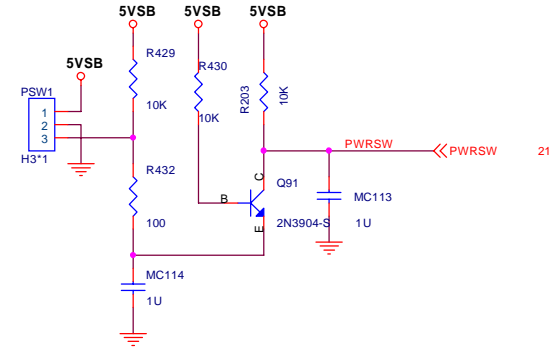
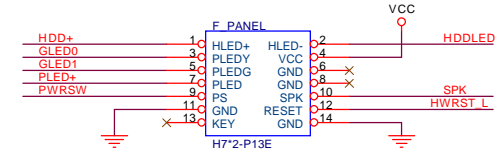
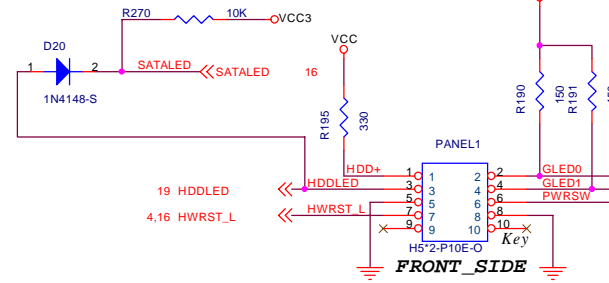
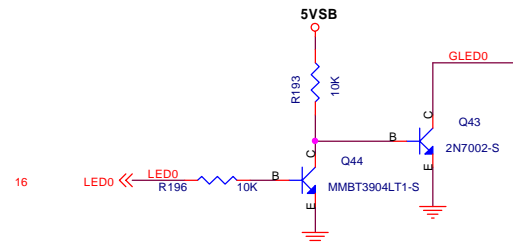
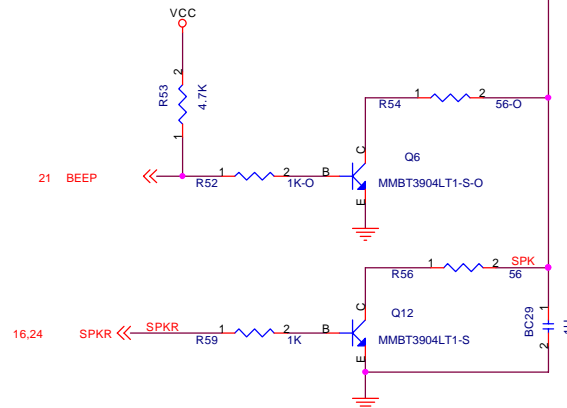
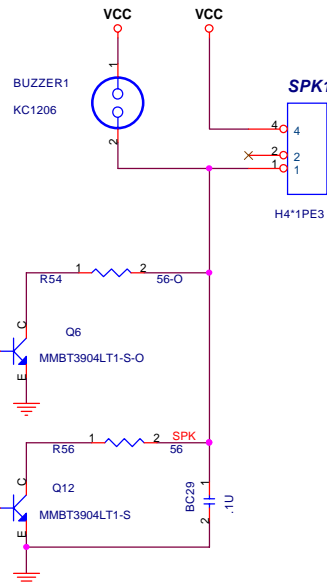
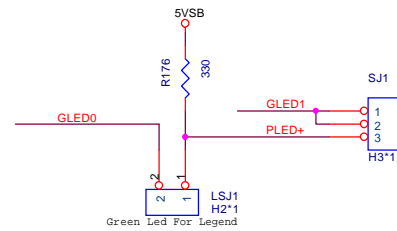




PWR CONN CAPS



	S0	S1	S3	S4,S5
PANEL1(4,2)	Green	G-blinking	Y-blinking	Dark
LPANEL1(3,5,7)	Green	G-blinking	G-blinking Yellow	Dark
LSJ1(1,2)	Dark	Dark	Light	Dark
SJ1(1,3)(2,3)	Light	Blinking	Blinking	Dark
GLED0 GLED1	HIGH LOW	HIGH SWITCH	LOW SWITCH	HIGH HIGH

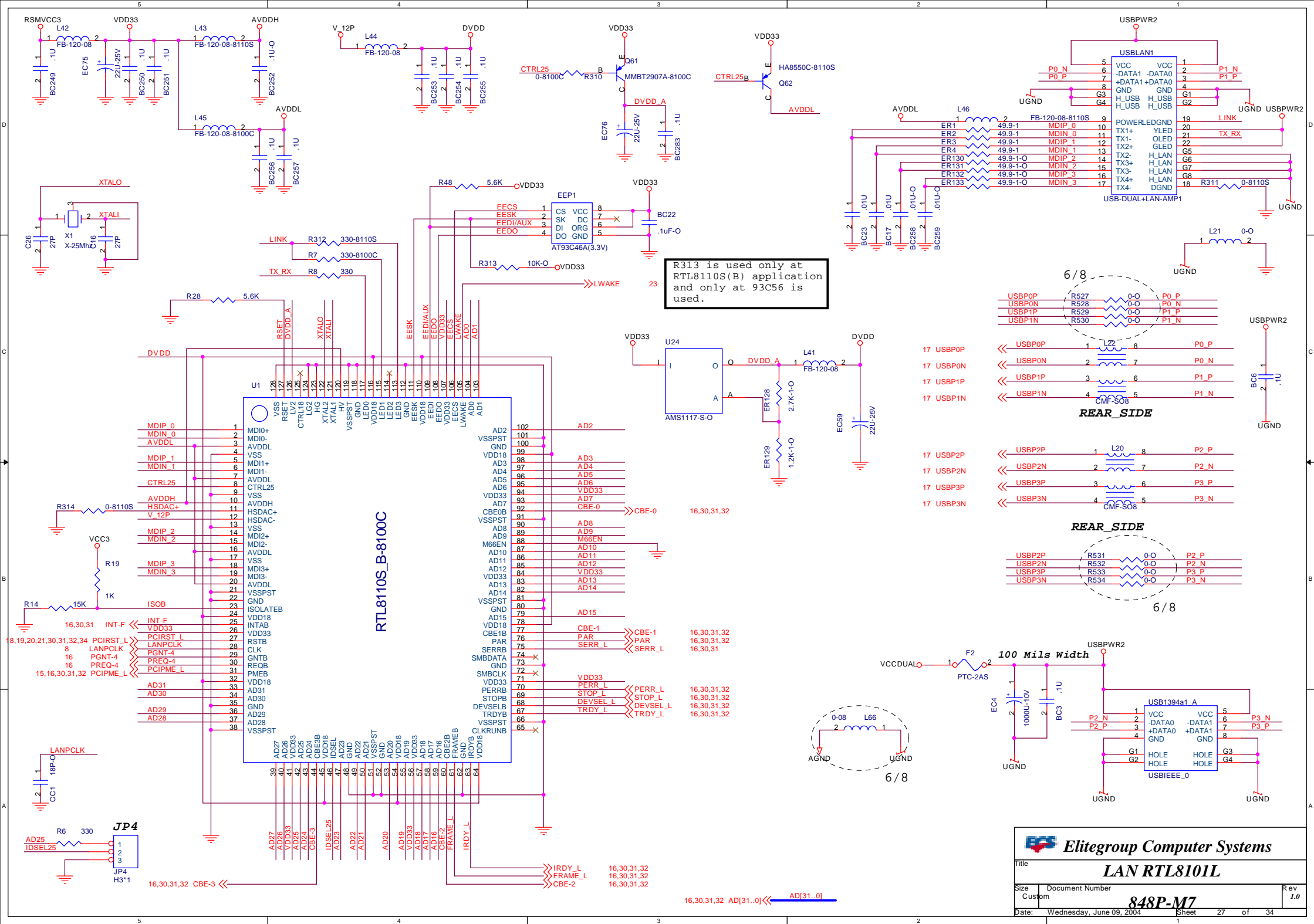


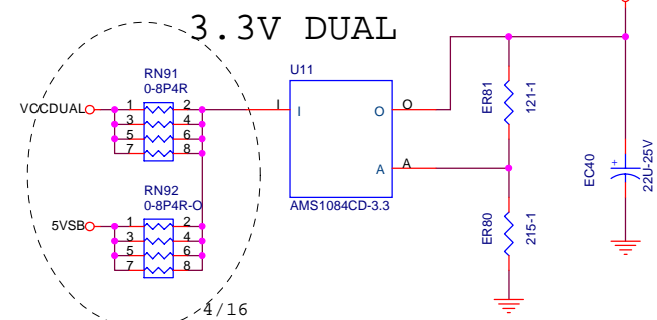
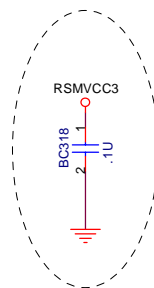
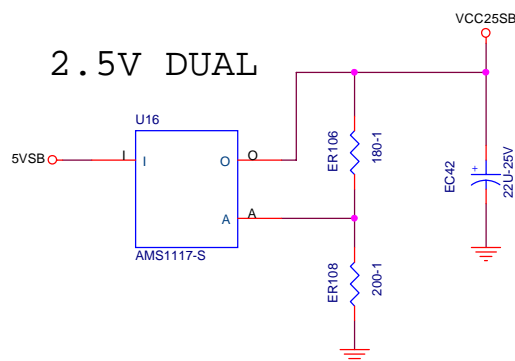
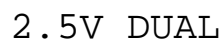
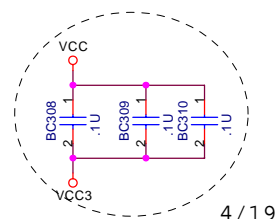
**Elitegroup Computer Systems**

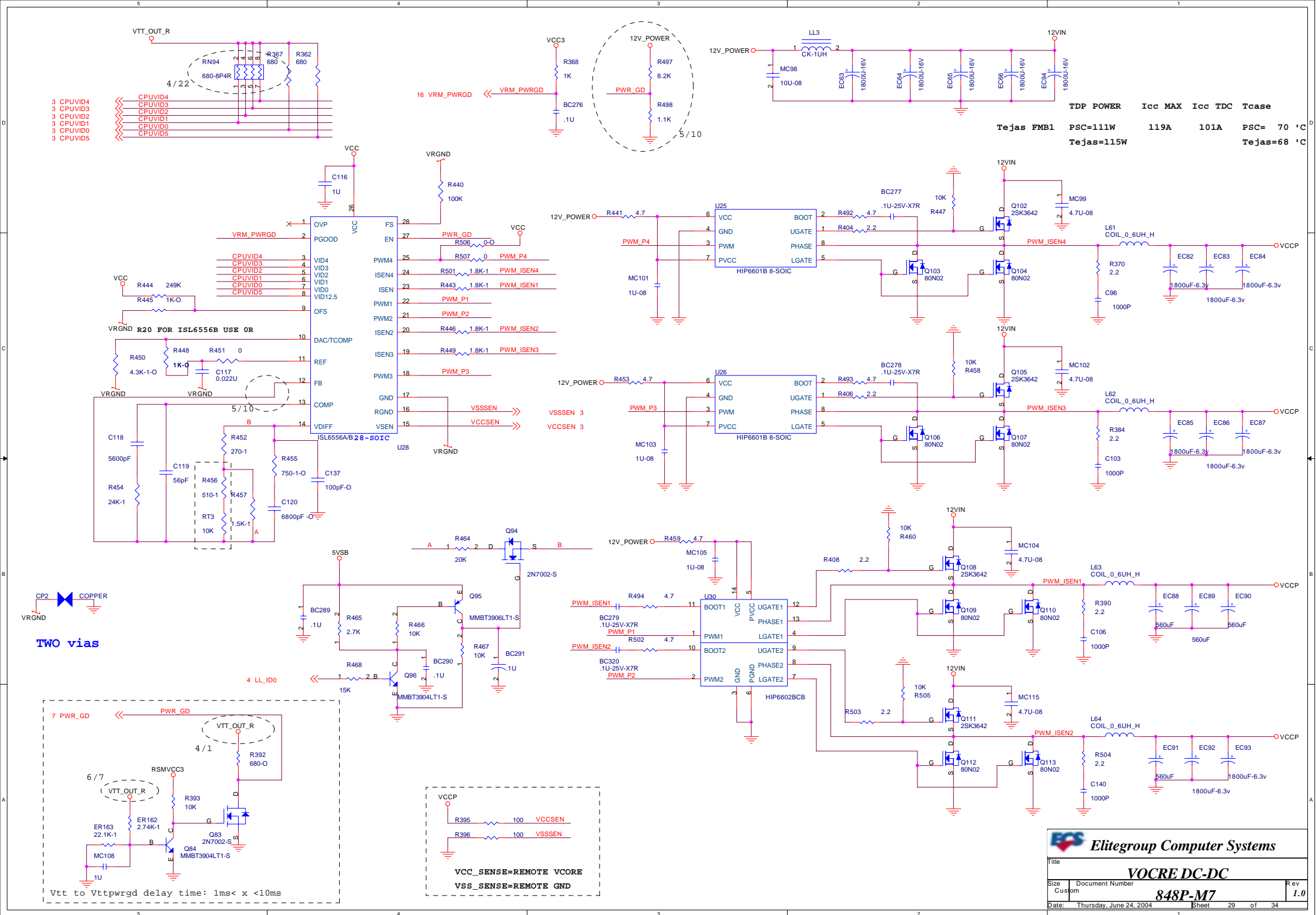
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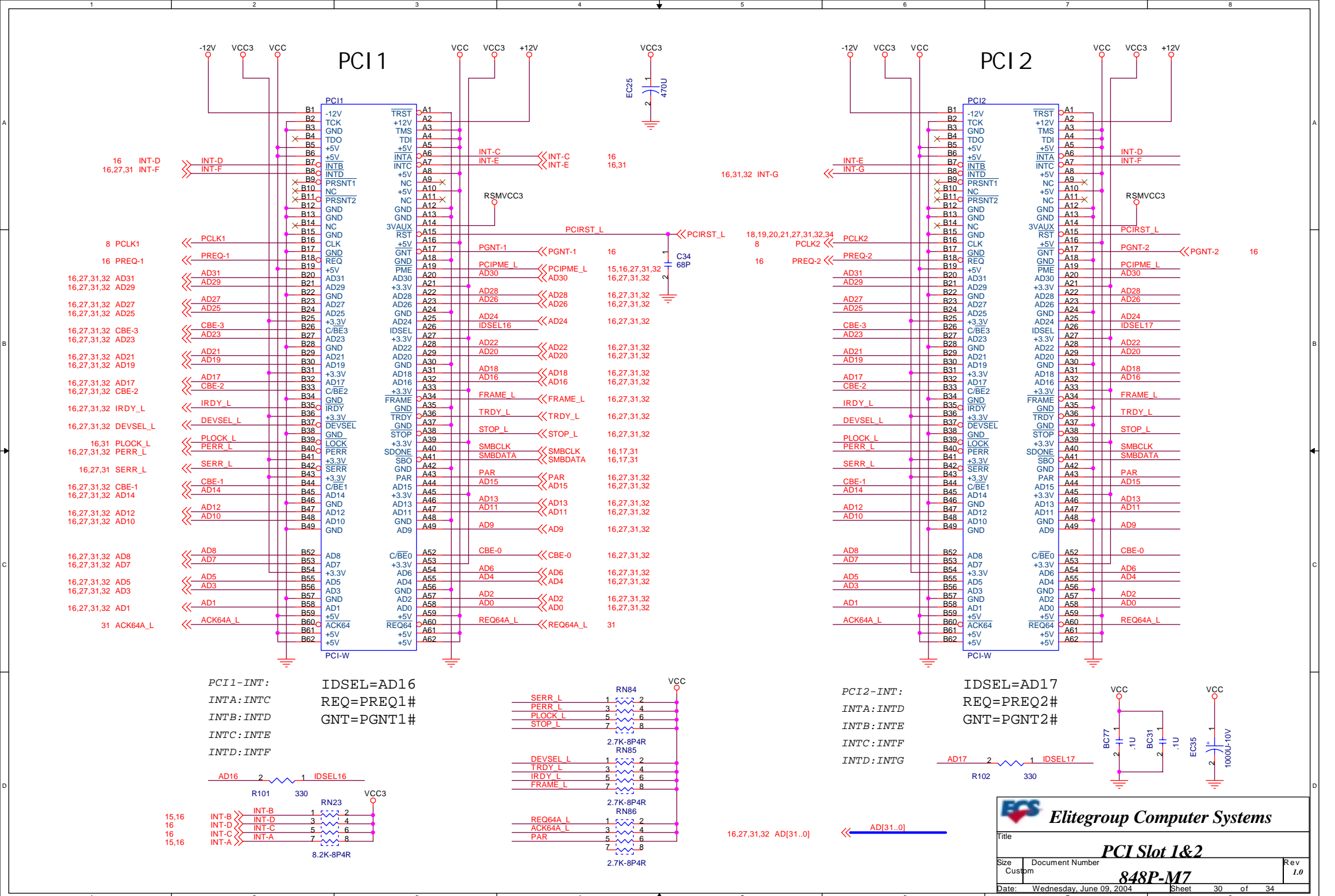
Size: Custom Document Number: **848P-M7** Rev: 1.0

Date: Friday, June 18, 2004 Sheet: 26 of 34





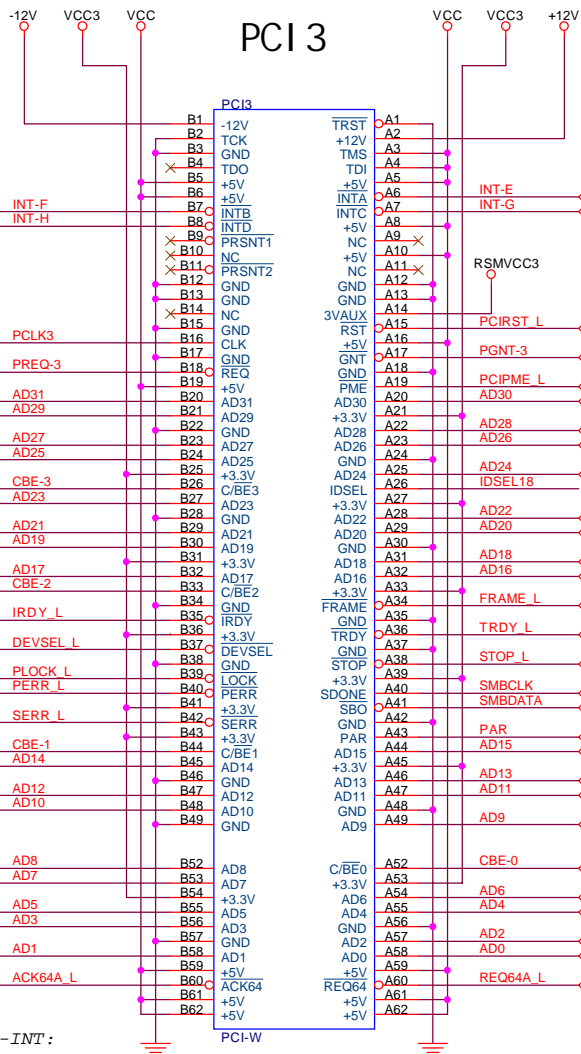




16,27,30,32 AD[31..0]

AD[31..0]

PCI 3



PCI 3-INT:

INTA: INTF

INTB: INTF

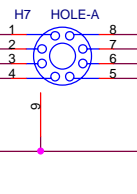
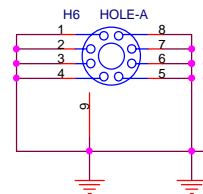
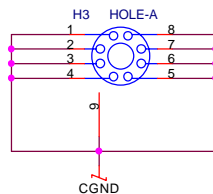
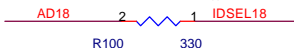
INTC: INTG

INTD: INTH

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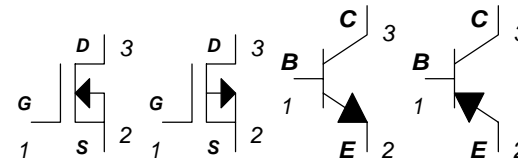
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GNT=PGNT2#

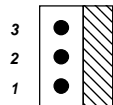


Top View

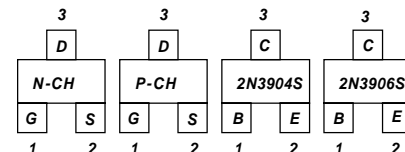
SOT-23



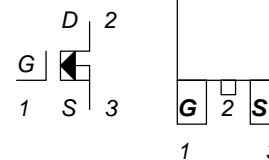
Fan Header



AMP640456-3

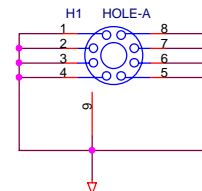
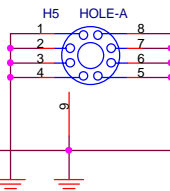
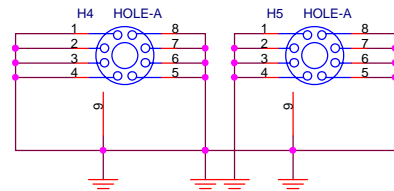
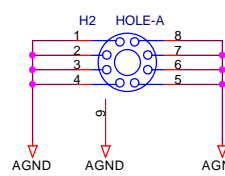


TO-263 / TO-252

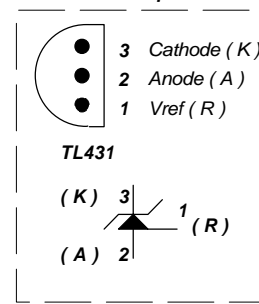


2

D



Top View



Elitegroup Computer Systems

Title		
PCI Slot 3		
Size	Document Number	Rev
B	848P-M7	1.0
Date:	Wednesday, June 09, 2004	Sheet 31 of 34





W:S/ 8:8.  
20 mil spacing to others.

